

Figure 1b

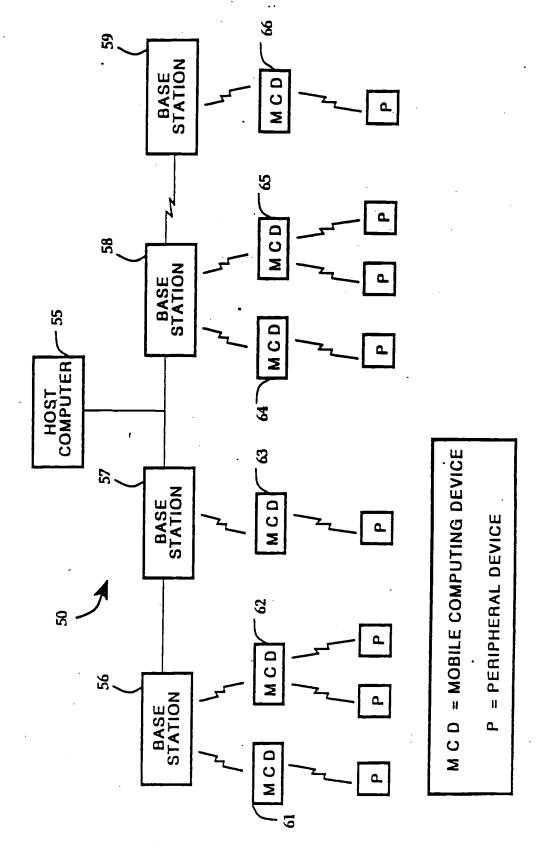


Figure 1c

Figure 2

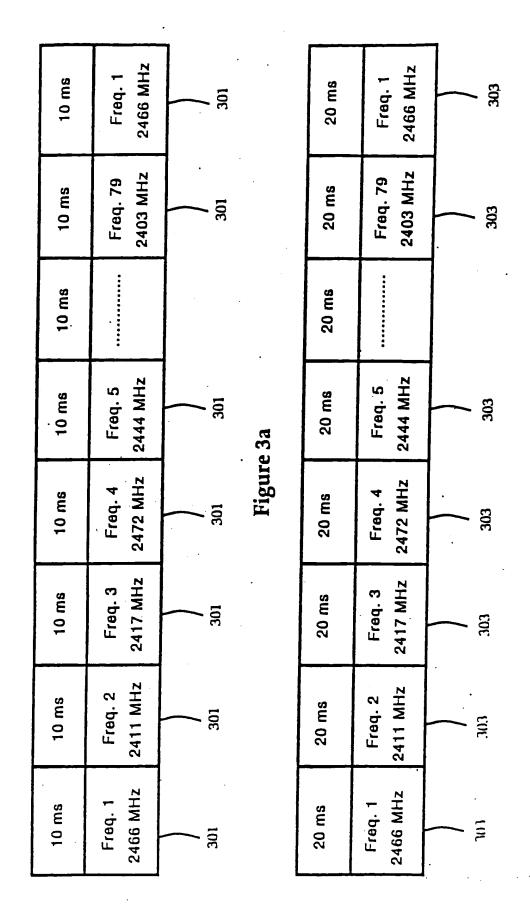


Figure 3b

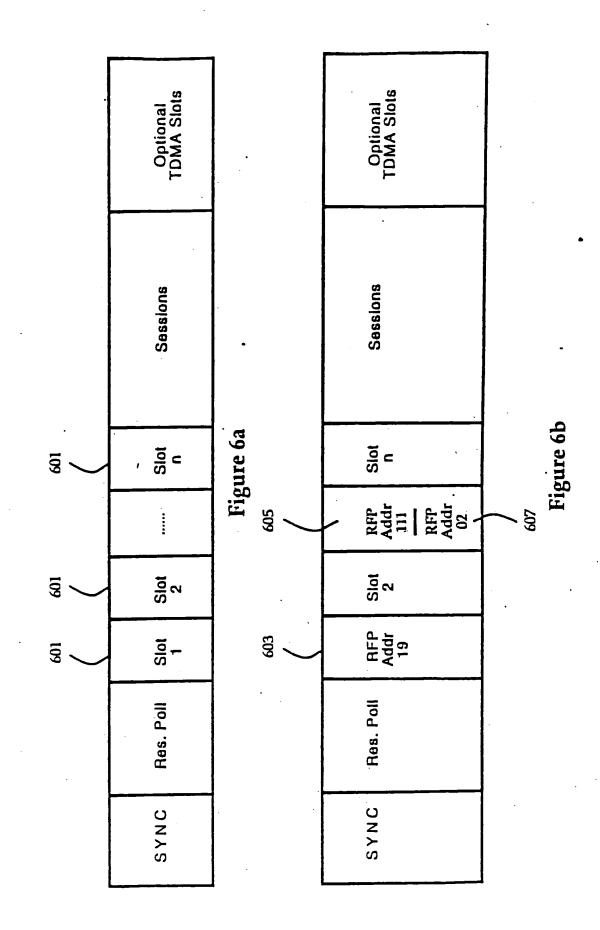
Freq 1: 2466 MHz	466 MHz	Freq 2: 2411 MHz	411 MHz	Freq 3: 2417 MHz	417 MHz	Freq 5:	Freq 5: 2444 MHz
Al. 1 20 ms	Al. 2 20 ms	Al. 1 20 ms	Al. 2 20 ms	Al. 1 20 ms	AI. 2 20 ms	Al. 1 20 ms	AI. 2 20 ms
401							

Figure 4a

1	Freq 1: 2466 MHz	36 MHz			Freq 2:	Freq 2: 2411 MHz	
Al. 1 20 ms	AI. 2 20 ms	AI. 3 20 ms	Al. 4 · 20 ms	Al. 1 20 ms	Al. 2 20 ms	Al. 3	AI. 4
							CO 1113
40I				,			

Figure 4b

Figure 5b



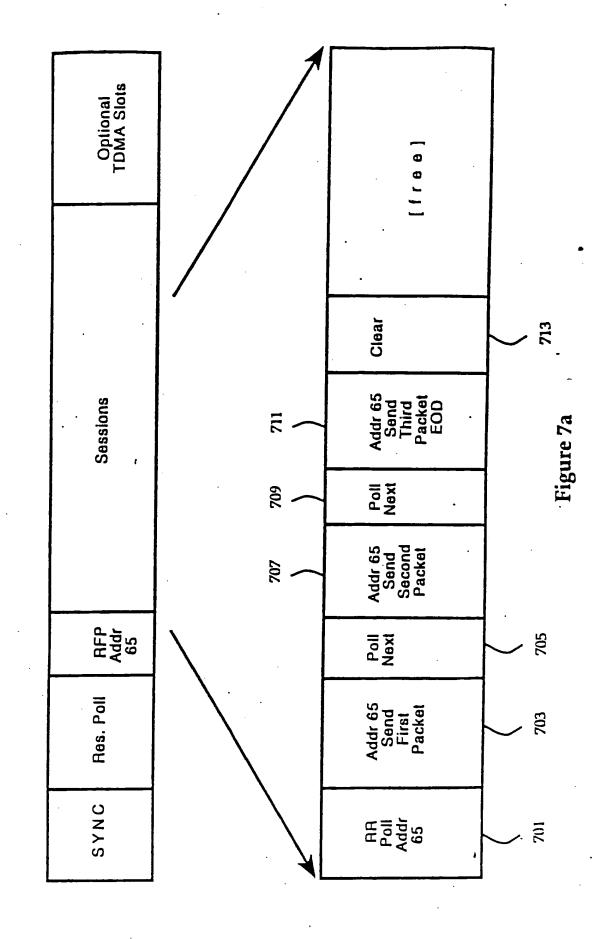
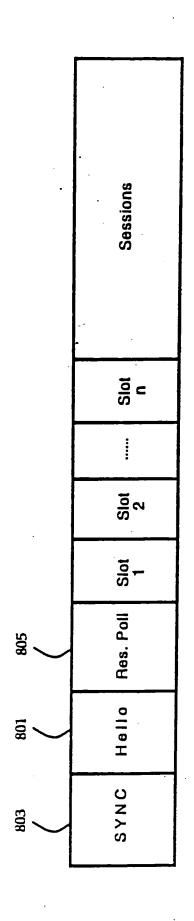
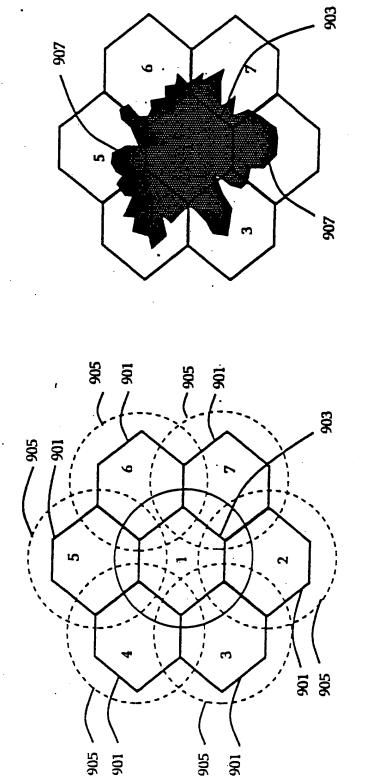
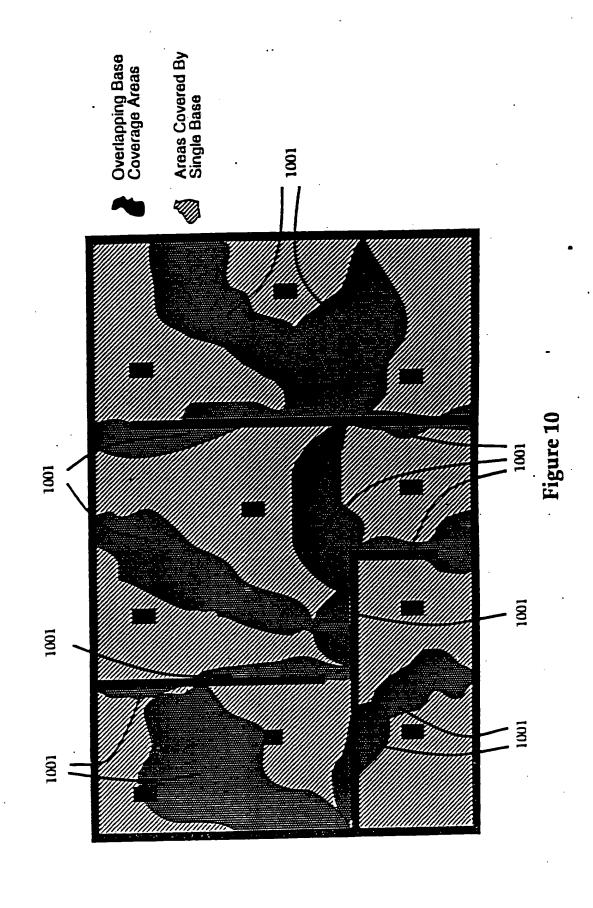
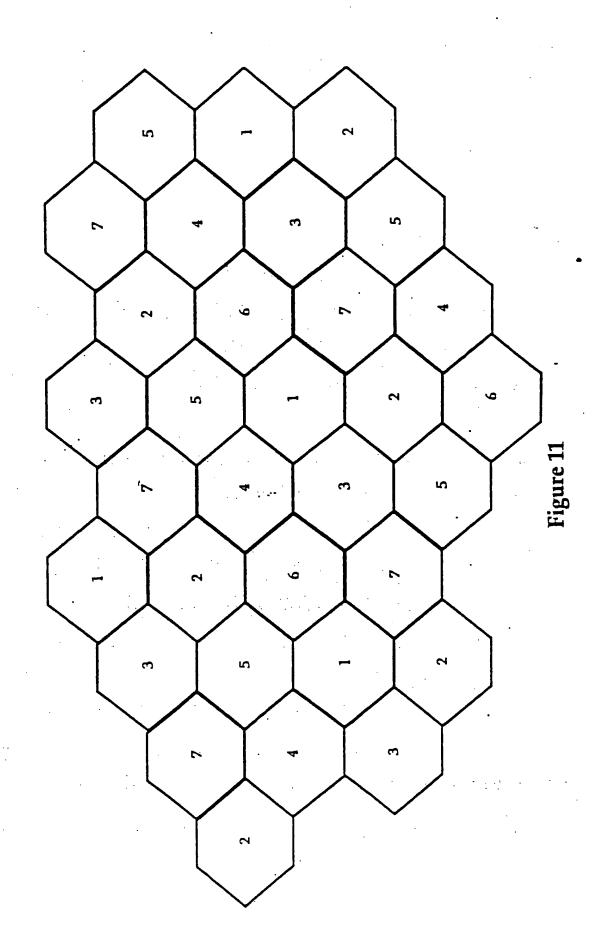


Figure 7b









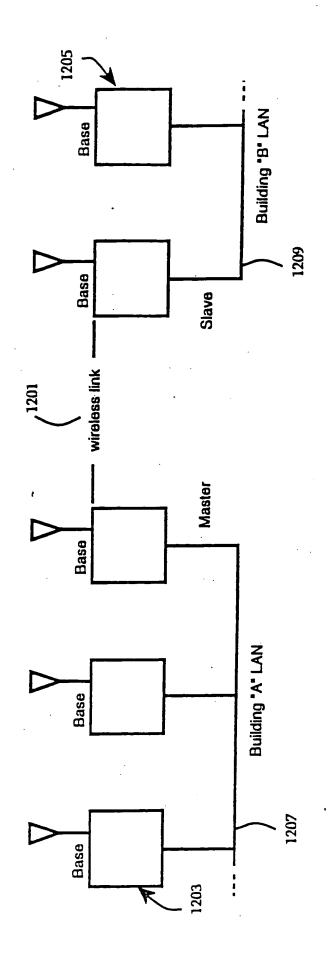
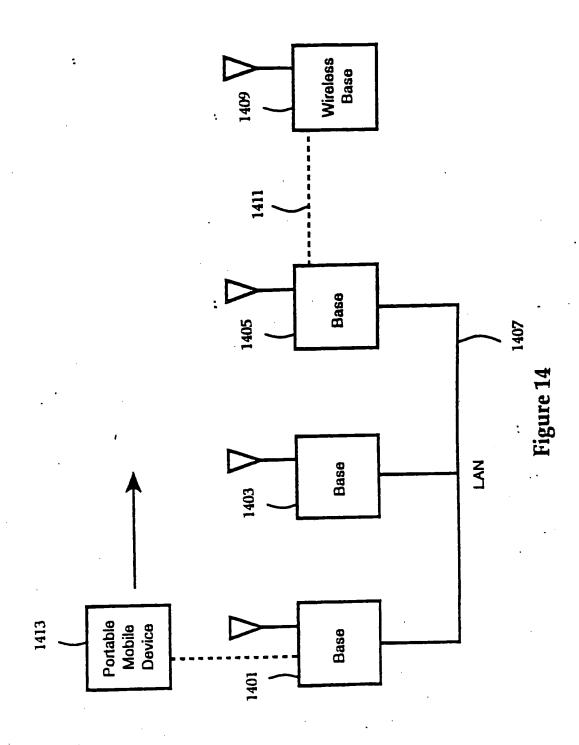


Figure 12

Figure 13



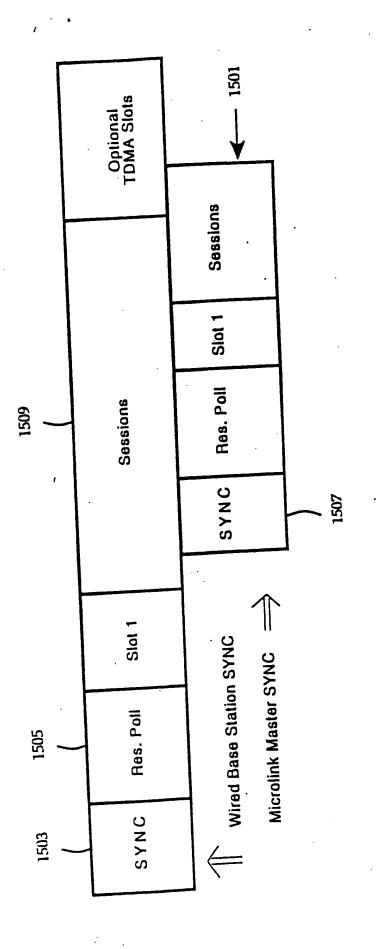


Figure 15

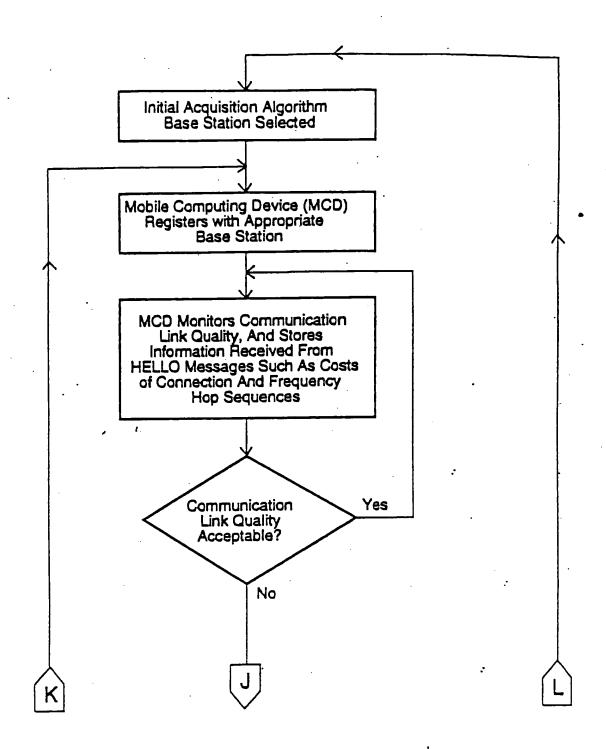
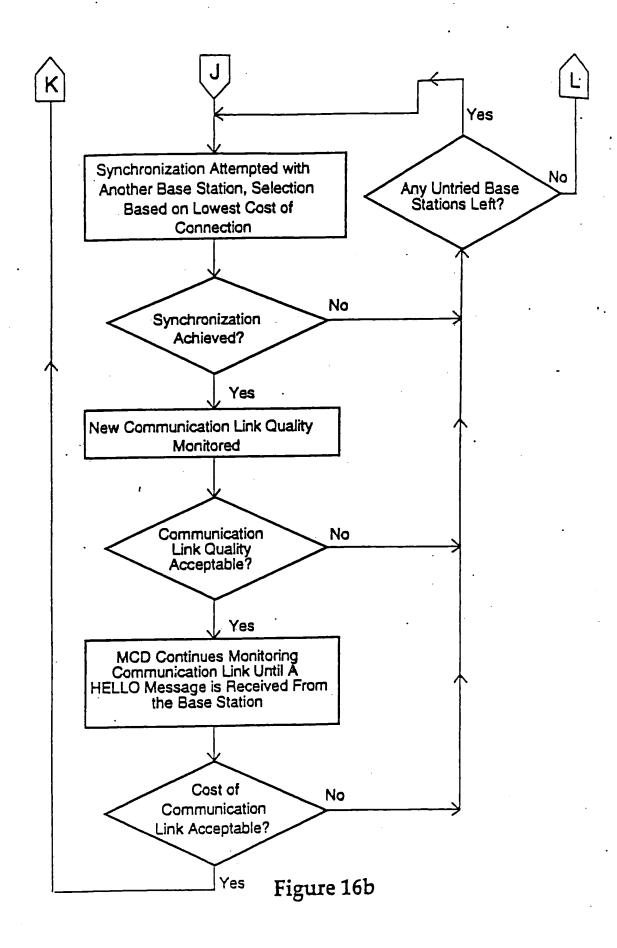


Figure 16a



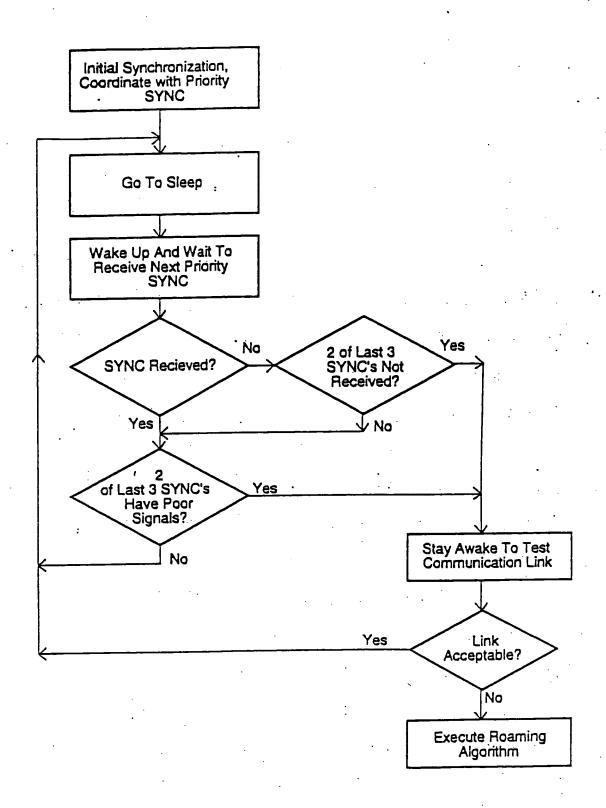
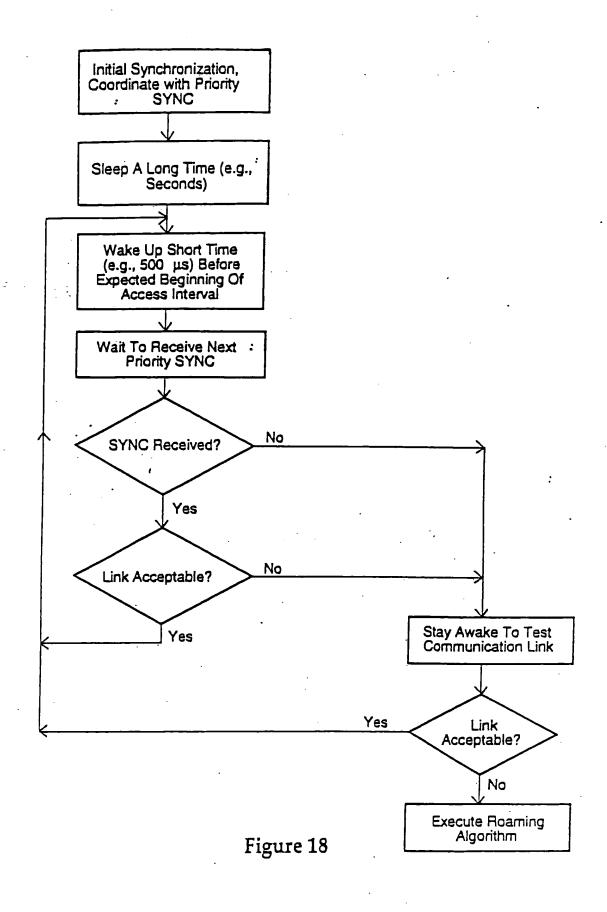
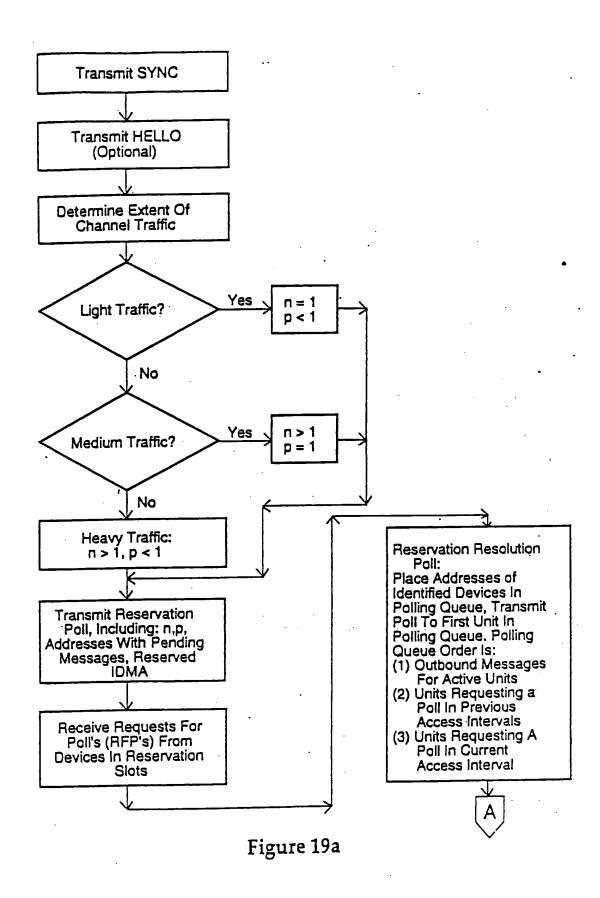


Figure 17





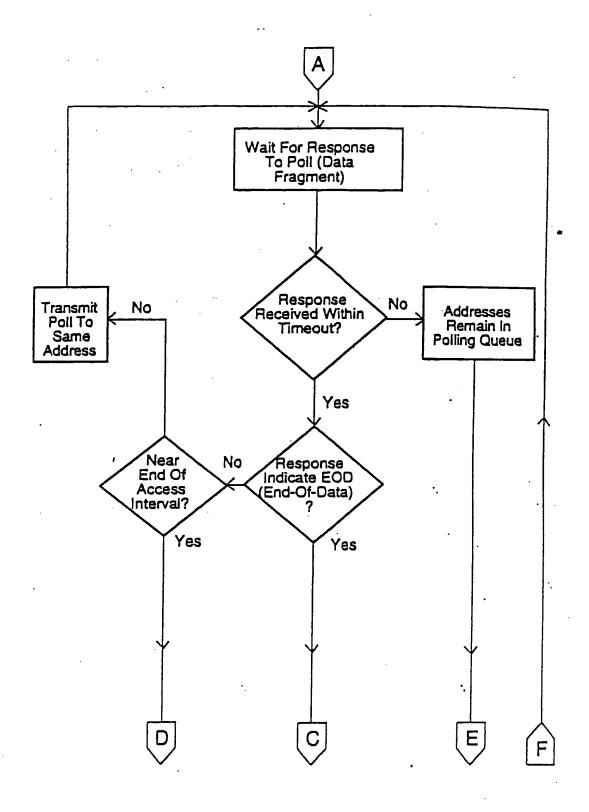


Figure 19b-1

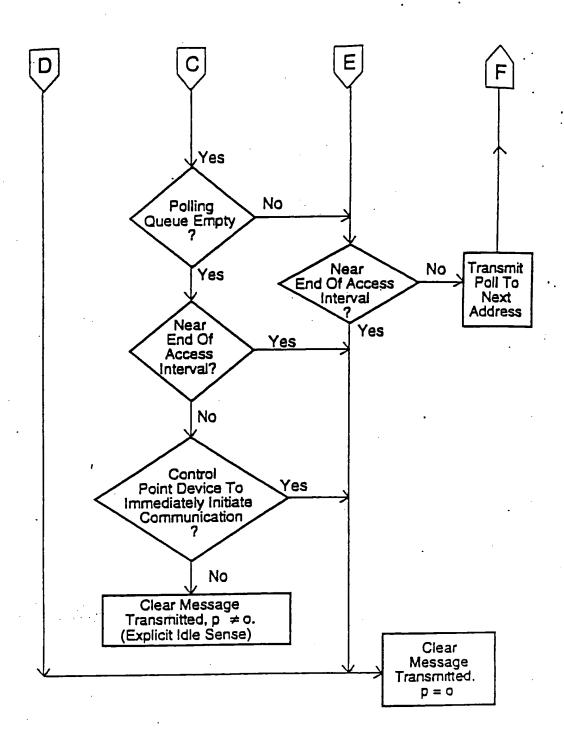


Figure 19b-2

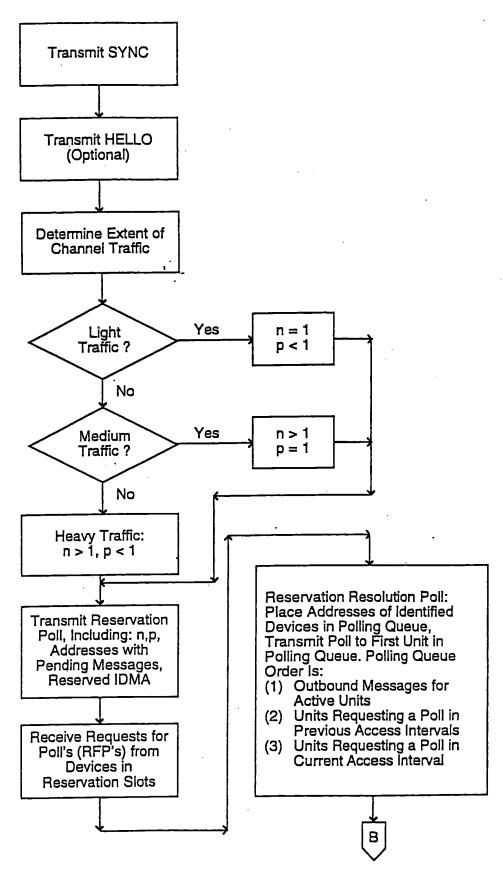


Fig. 20a

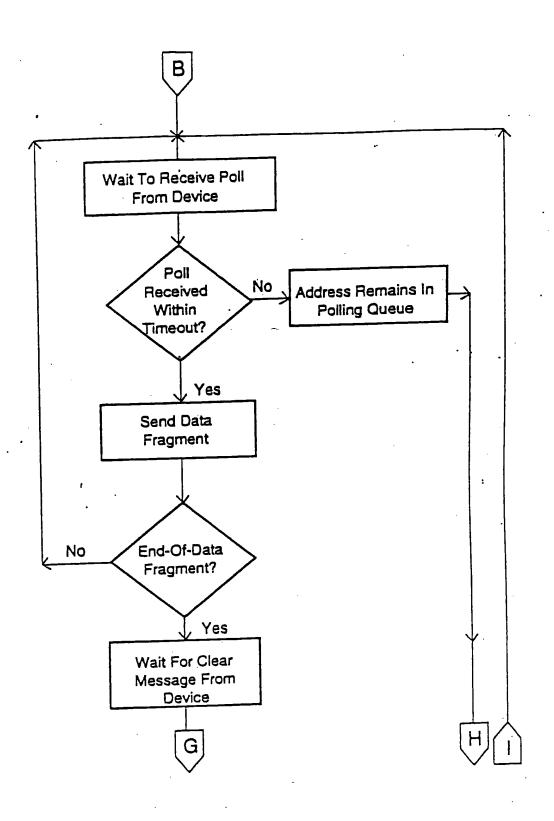


Figure 20b-1

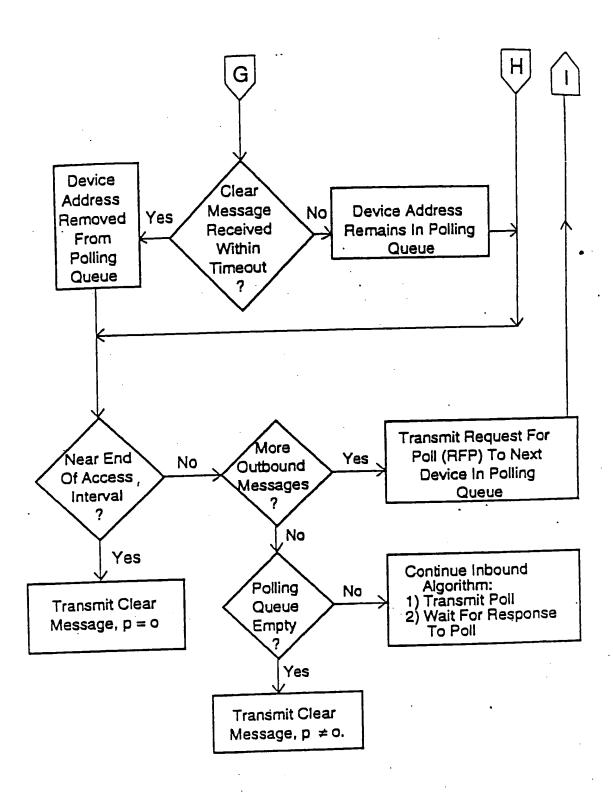
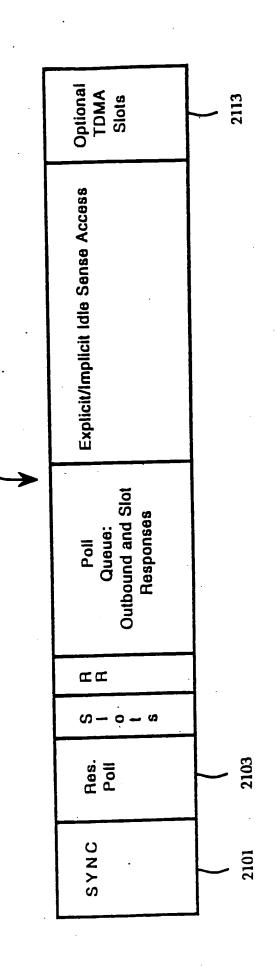


Figure 20b-2



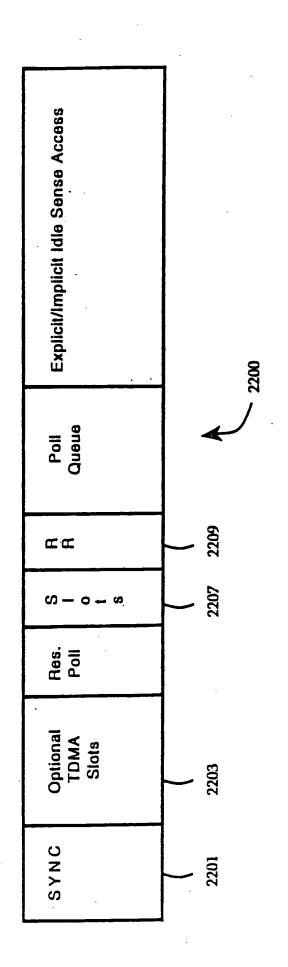


Figure 22

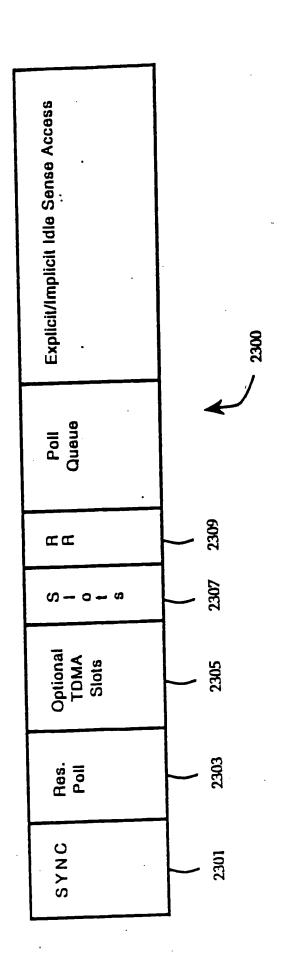


Figure 23

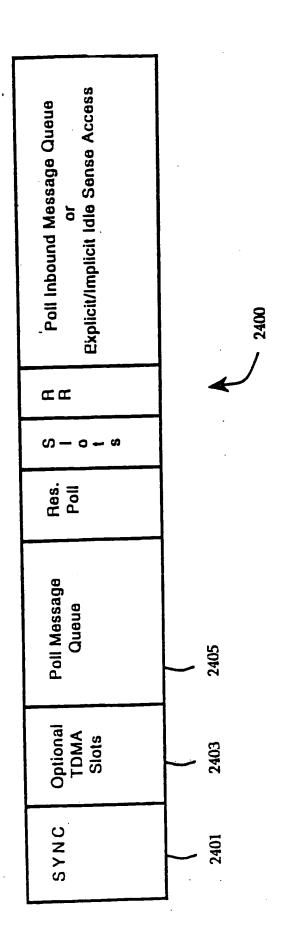
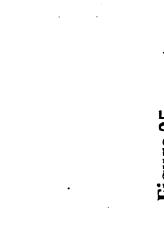
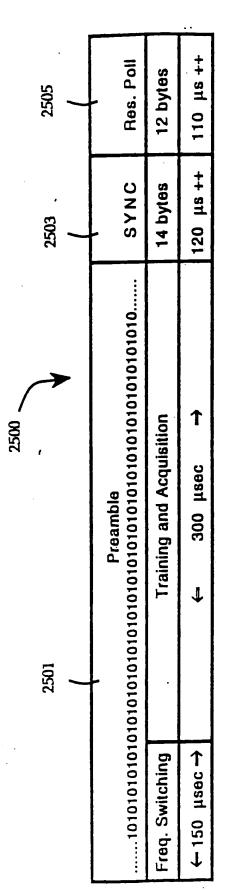


Figure 24





++ allows additional bit times for HDLC zero bit insertion

SYNC

7 2600

Вуте	Content	Definition
1	FLAG	· Frame Delimiter
2	Dest. Address 1	Destination Address (SYNC = Broadcast = FFFF _H)
3	Dest. Address 2	Destination Address, 2nd Byte
4	ТҮРЕ	Message Type=SYNC, Priority Sync, or Priority SYNC with
5	Source Address 1	Control Point Address
9	Source Address 2	Control Point Address, 2nd Byte
7*	SYSTEM ID	System Identifier (LAN ID)
₽8	NET ID	Net ID
•6	Command/Status 1	Control Point Status
		2 Bits Control Point Type
		2 Bits Loading Factor
		1 Bit Scheduled Service Availability
		3 Bits Reserved
10•	Command/Status 2	Scheduled Services
		5 Bits Number of Allocated TDMA Time Slots
٠		3 Bits Number of Available Time Slots
11"	Random	Randomly Selected Character for Improved Encryption
12	CRC 1	Error Check
13	CRC 2	Error Check
14	FLAG	Frame Delimiter

Figure 26

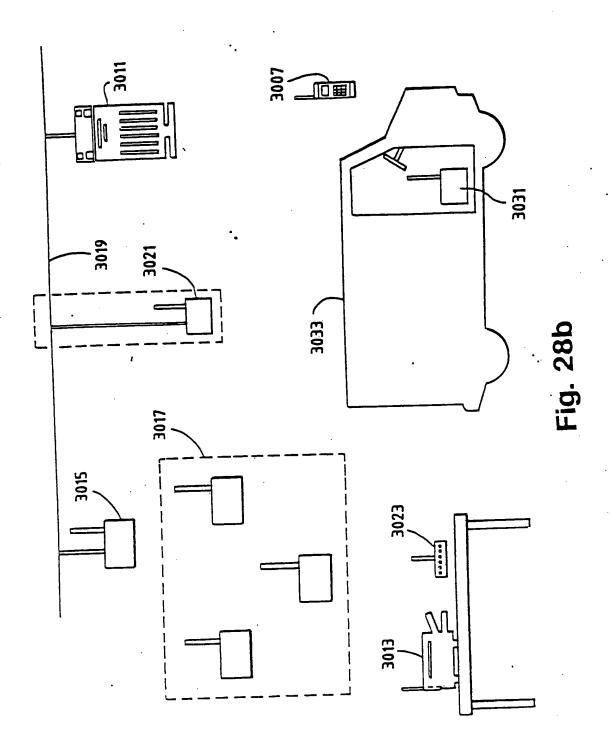
*Optionally Encrypted Bytes

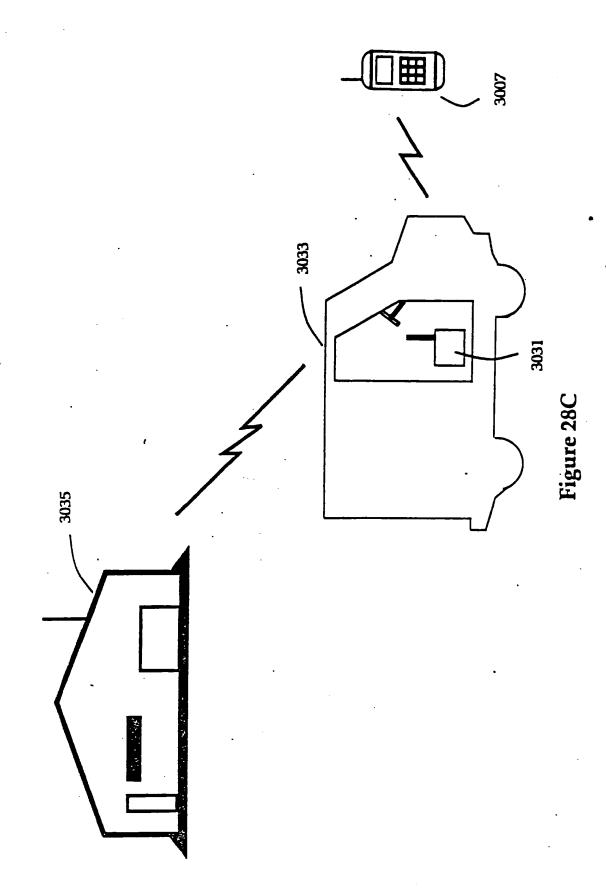
Reservation Poll

Definition	Frame Delimiter	Destination Address (RES = Broadcast = FFFF _H)	Destination Address, 2nd Byte	Message Type = RES POLL	3 Bits Number of Slots	5 Bits Probability Factor	Hopping Sequence ID	Hopping Sequence Index		3 Bits Hop Rate	2 Bits Al's per Hop	3 Bits Reserved	Error Check	Error Check	Frame Delimiter
Content	FLAG	Dest. Address 1	Dest. Address 2	ТУРЕ	SLOT PARMS		Sequence	Index	NET PARMS				CRC 1	CAC 2	FLAG
Вуте		2	3	4	5	9	7*	8.	.6				10	11	12

Figure 27

Fig. 28A





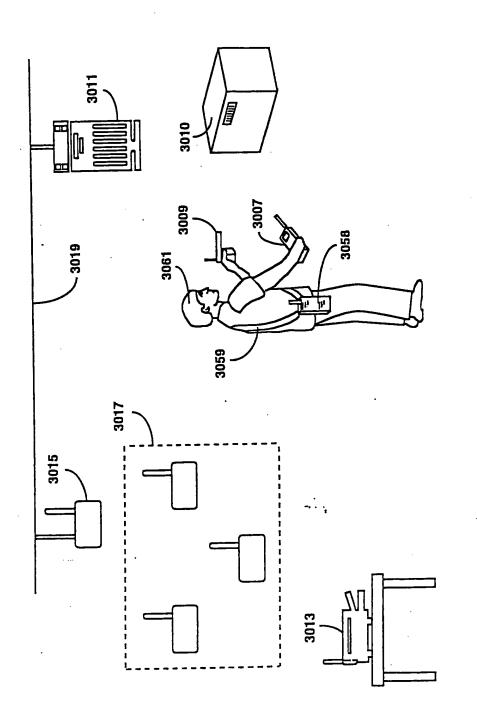


Fig. 29B

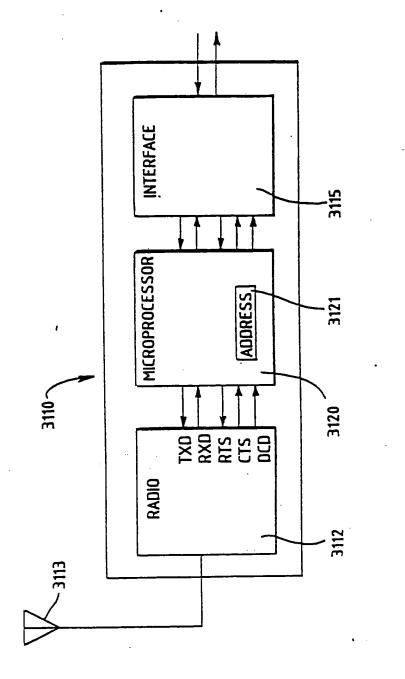


Fig. 30

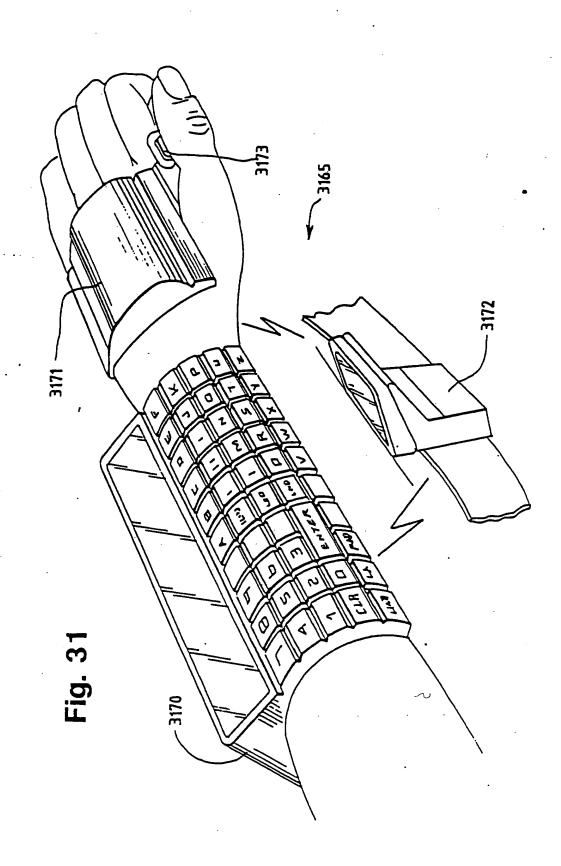
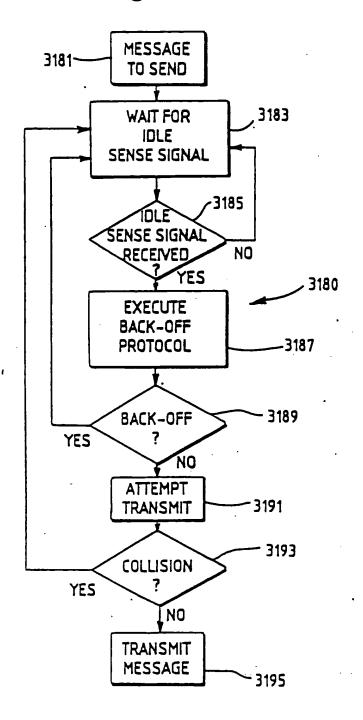


Fig. 32



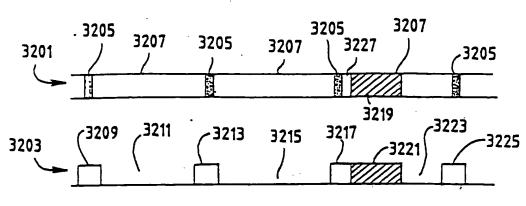
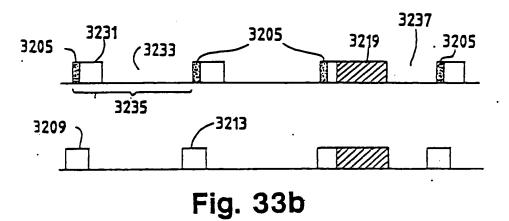


Fig. 33a



3231 3231 3231 3231 3233 3243 3245 3247 3213 3215 Fig. 33c

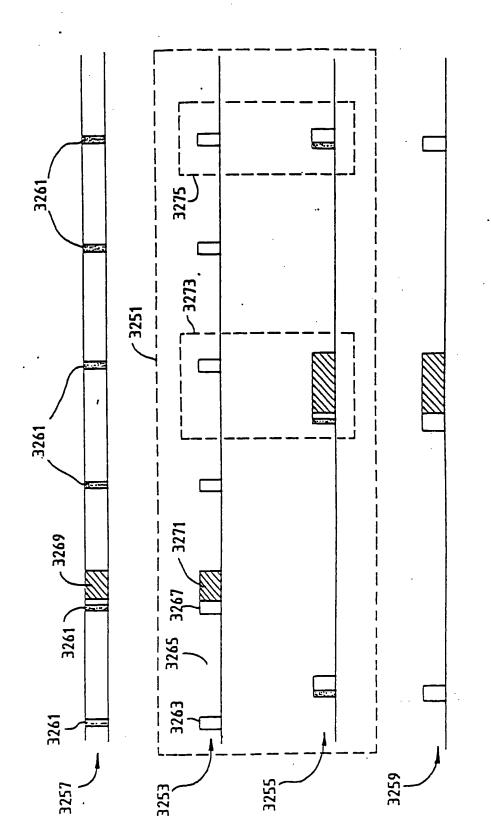


Fig. 34

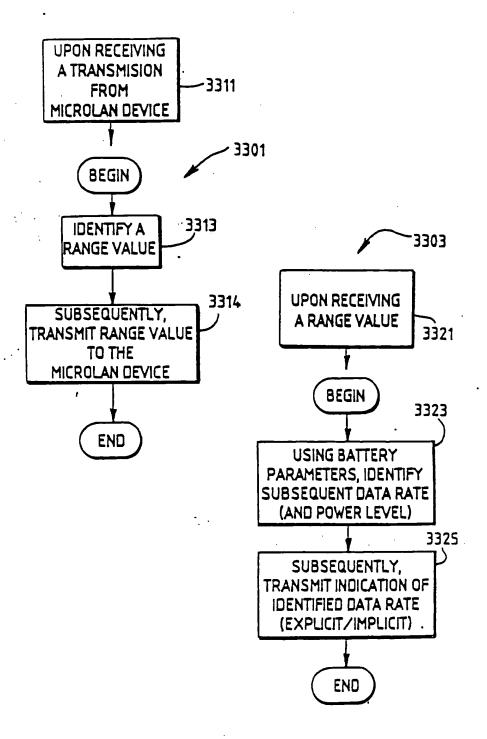
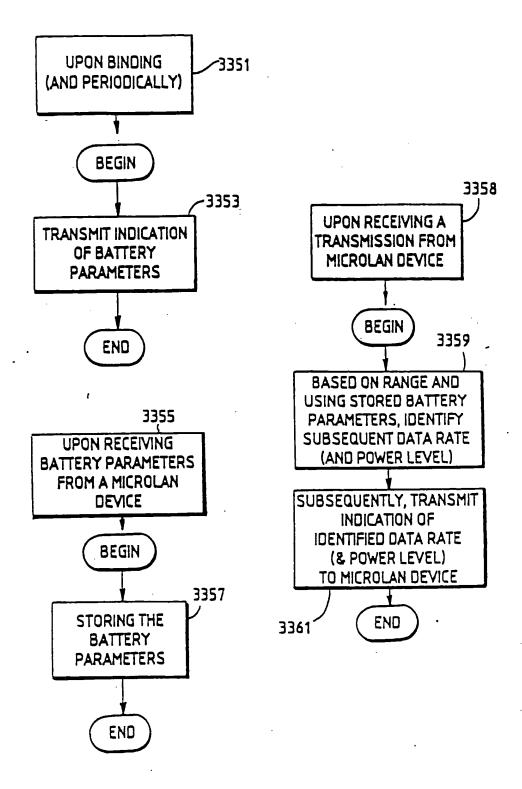


Fig. 35

Fig. 36



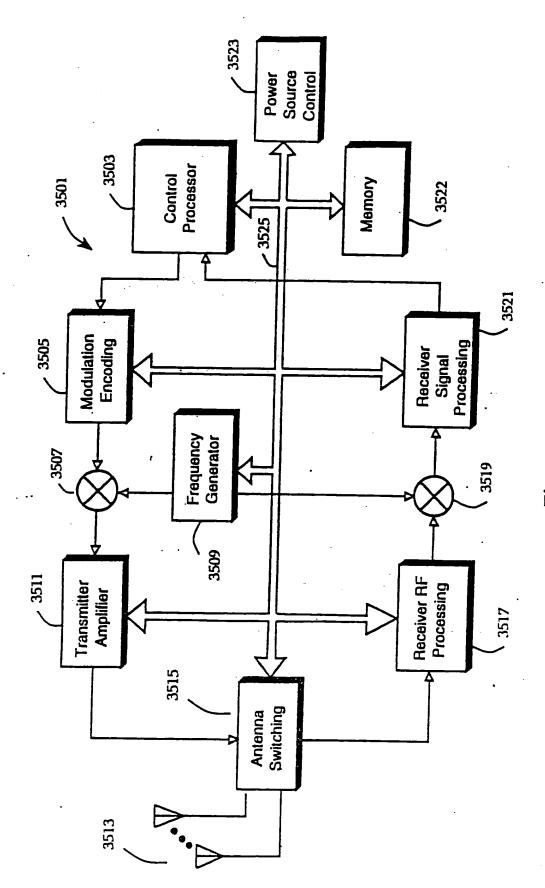


Figure 37

Figure 38

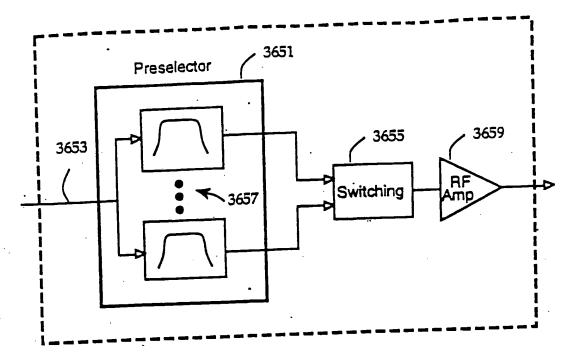


Figure 39

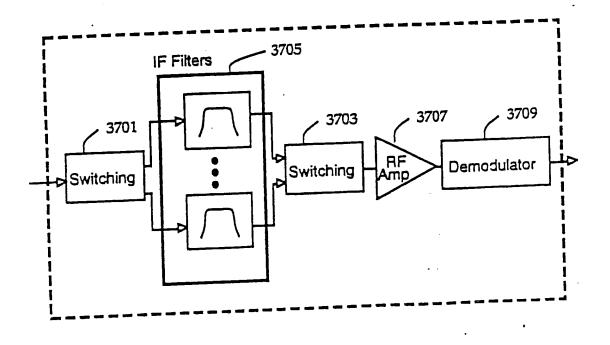


Figure 40

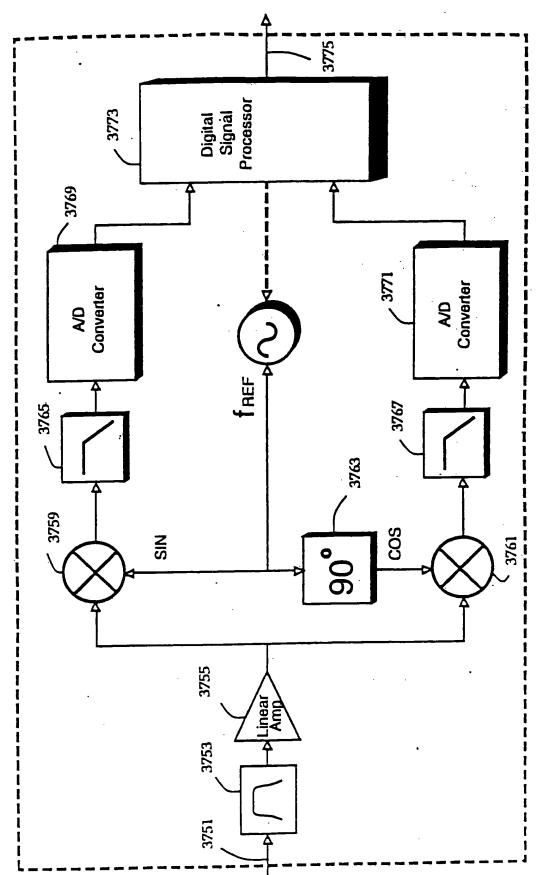


Figure 41

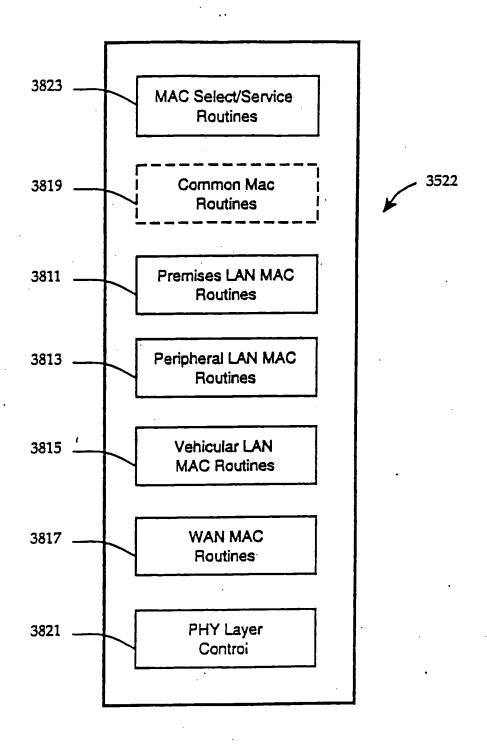
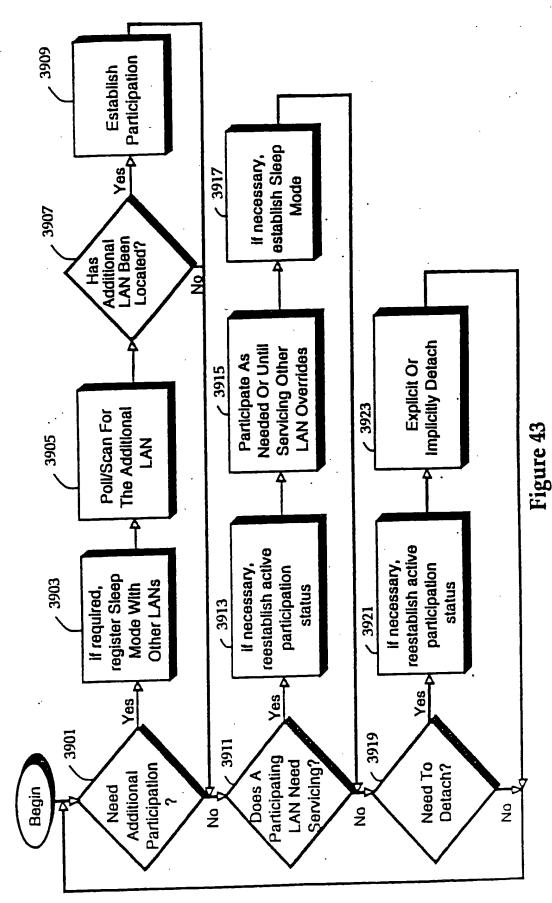


Figure 42



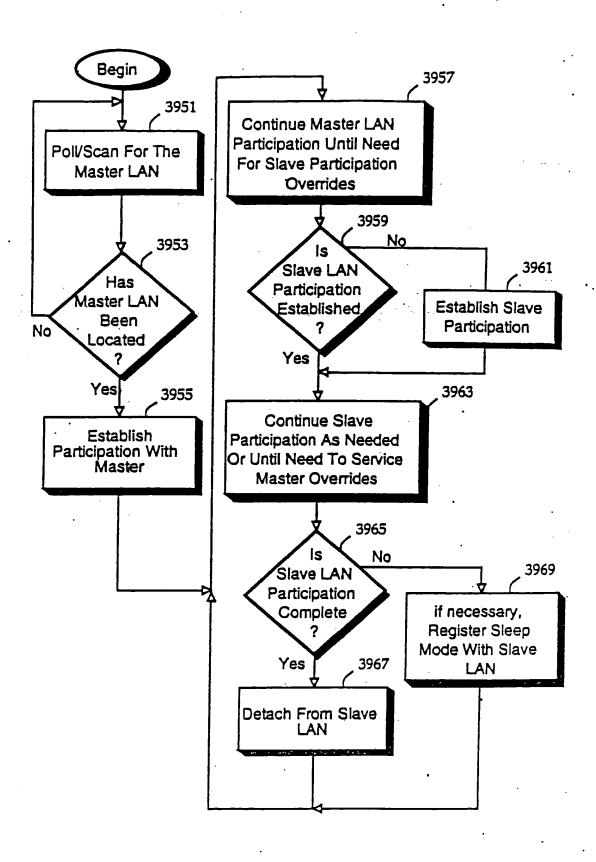


Figure 44

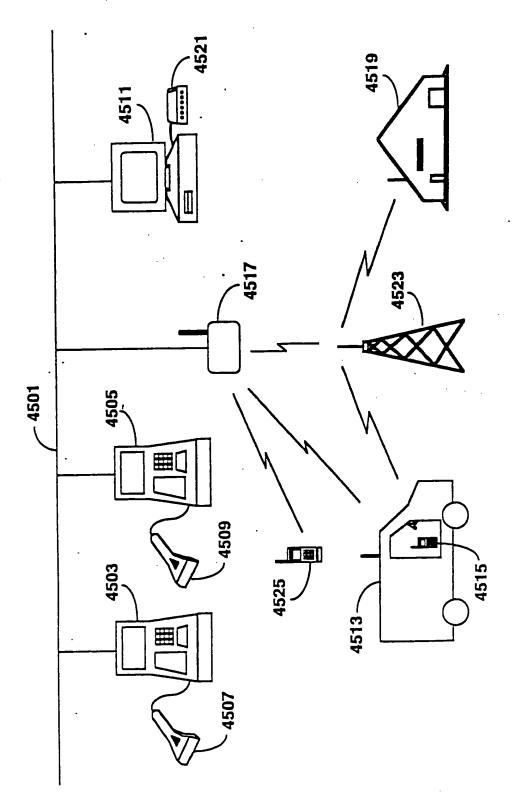


Fig. 45

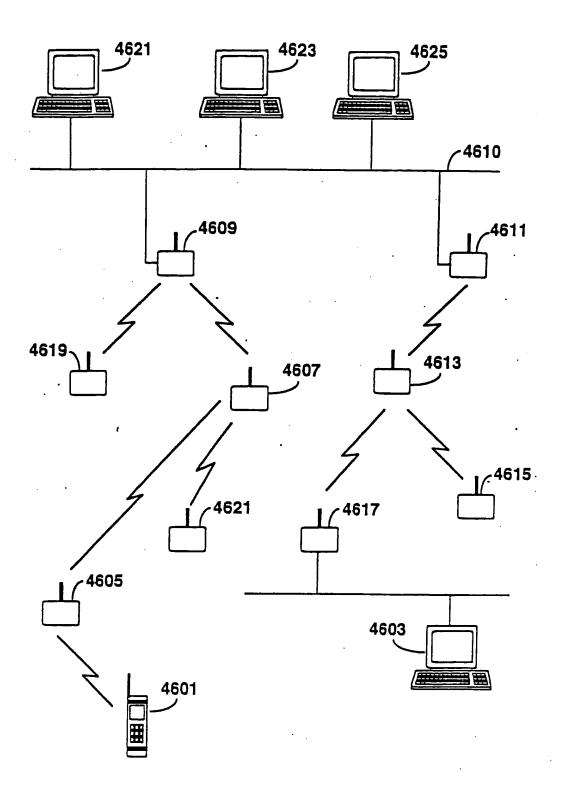


Fig. 46a

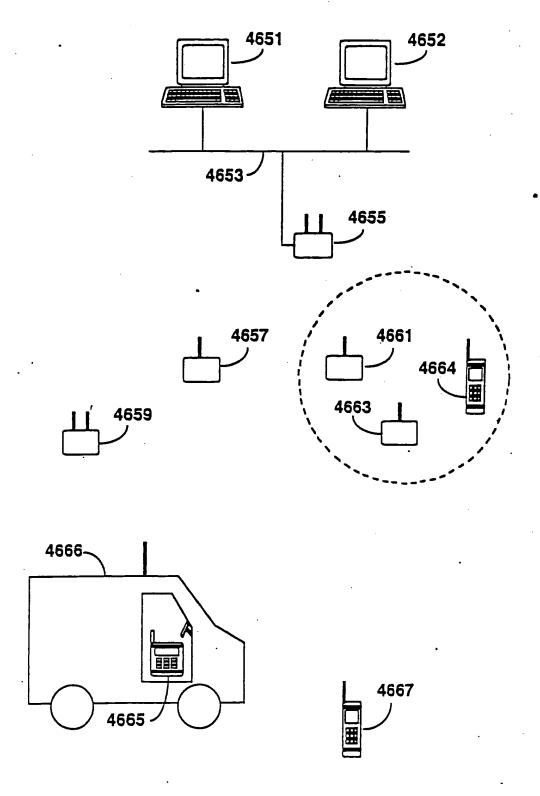


Fig. 46b

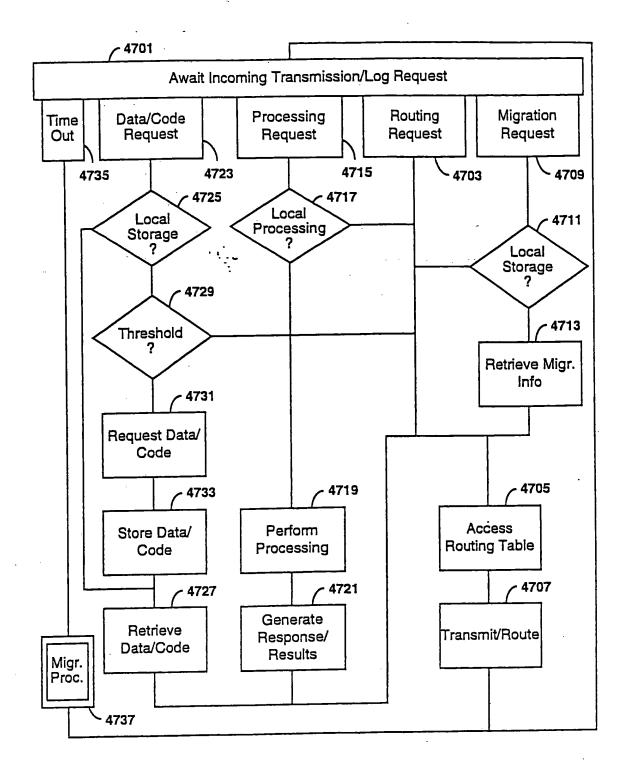


Fig. 47a

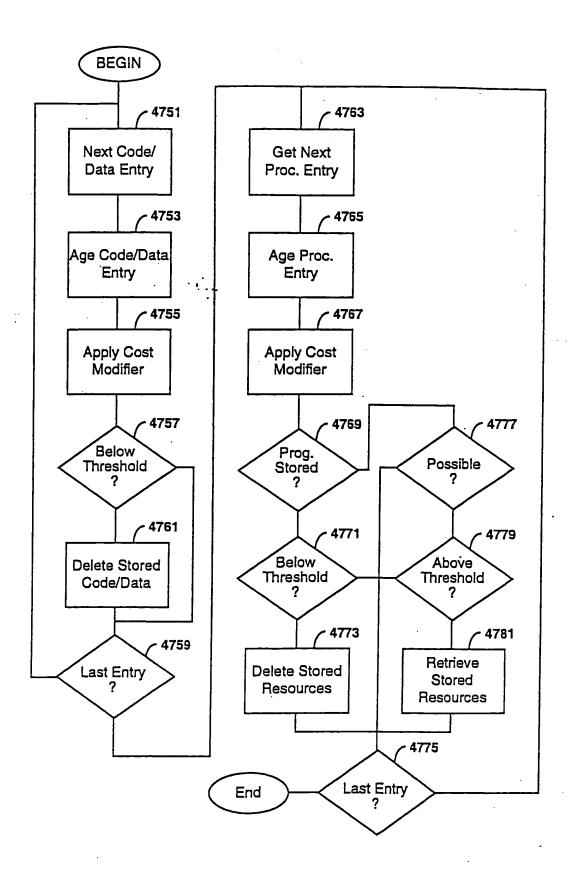


Fig. 47b

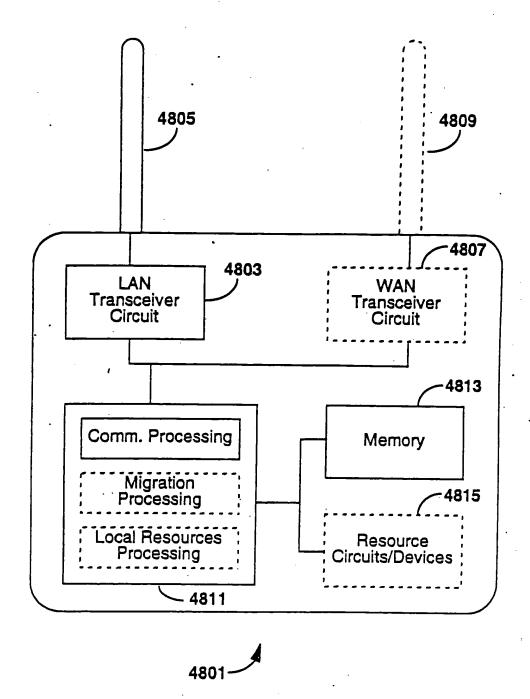


Fig. 48

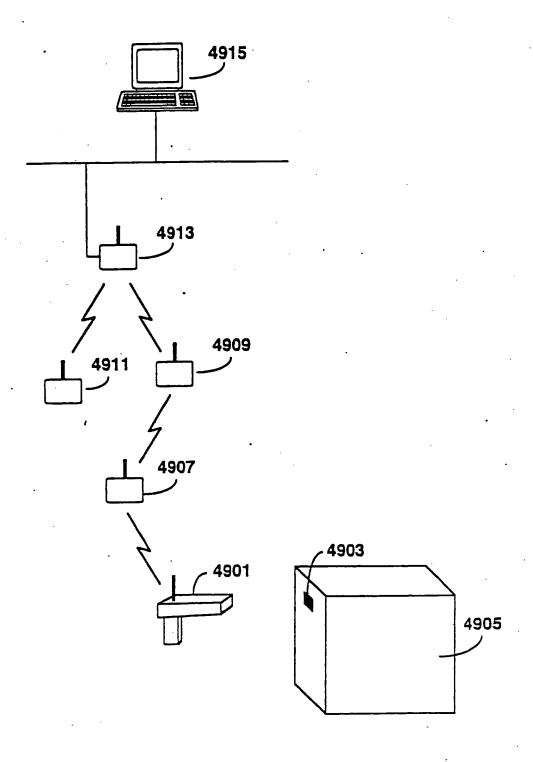


Fig. 49a

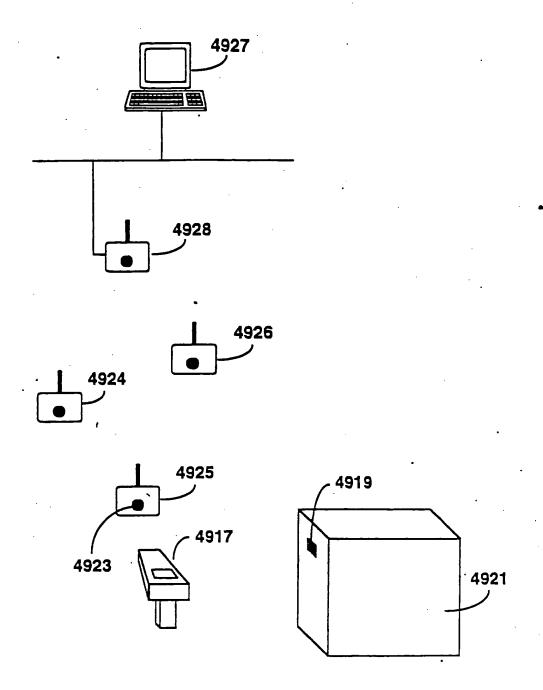


Fig. 49b

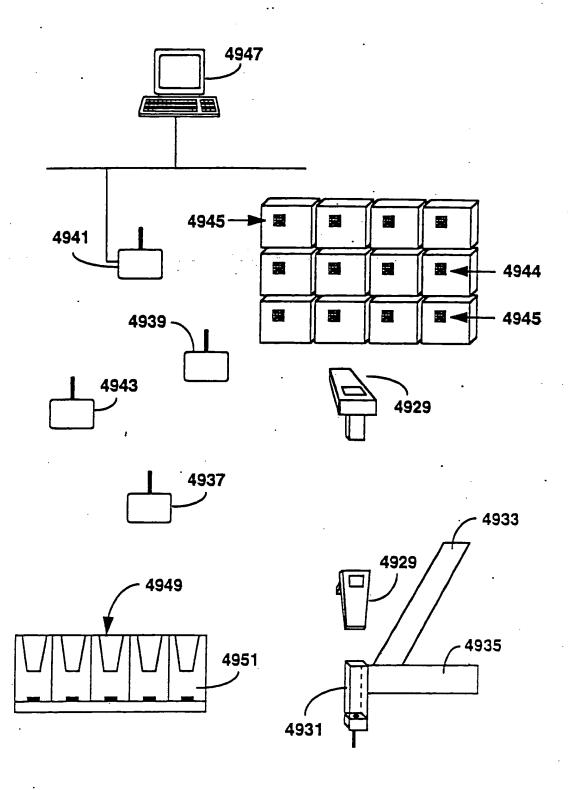


Fig. 49c

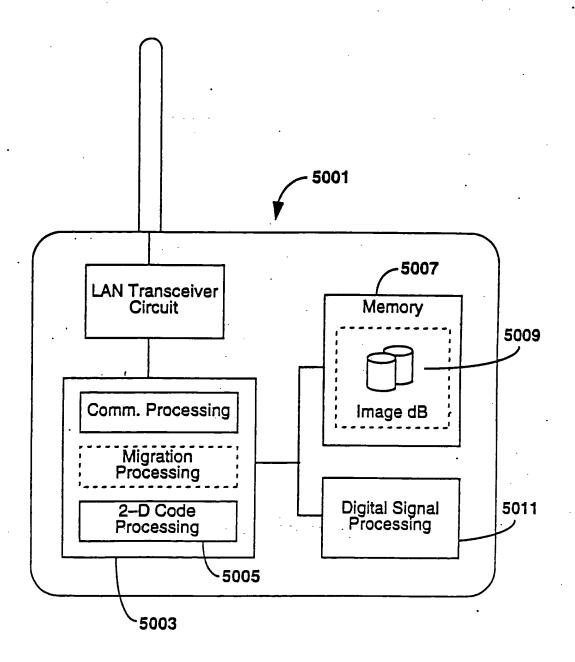


Fig. 50

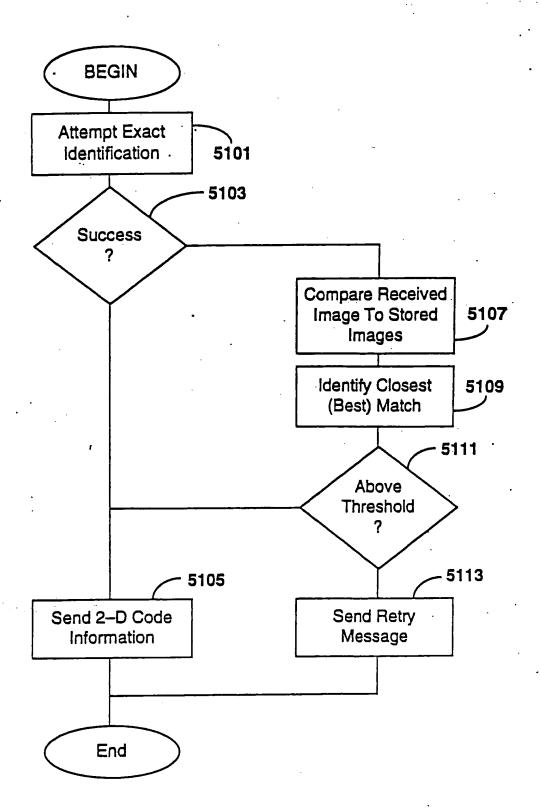


Fig. 51a

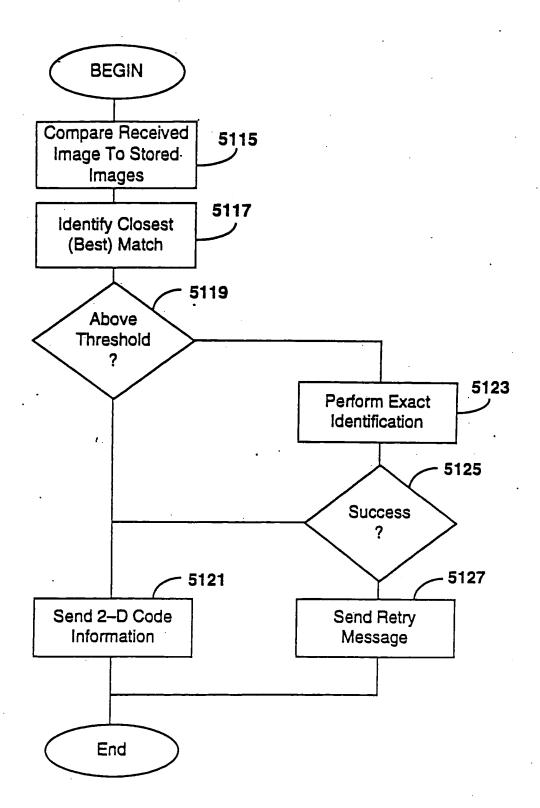
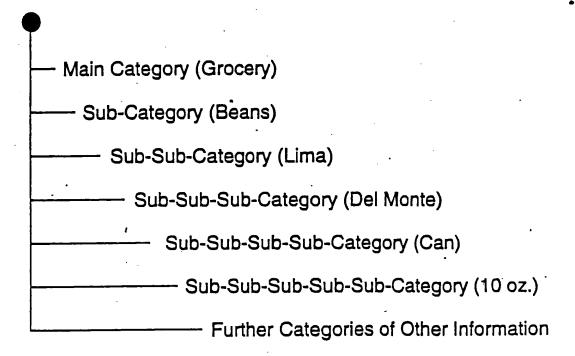


Fig. 51b



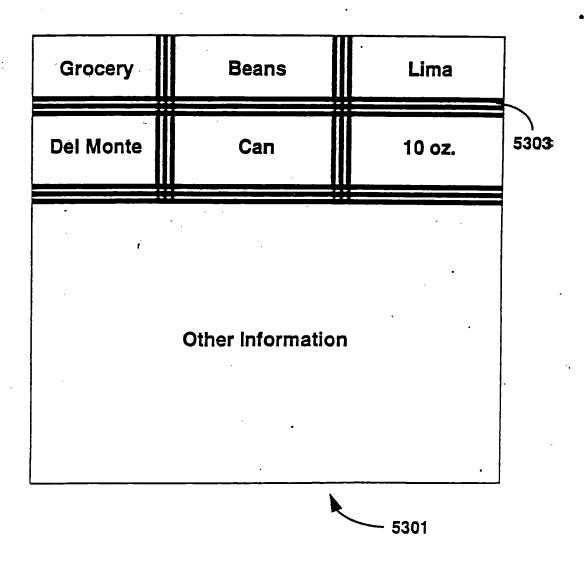


Fig. 53

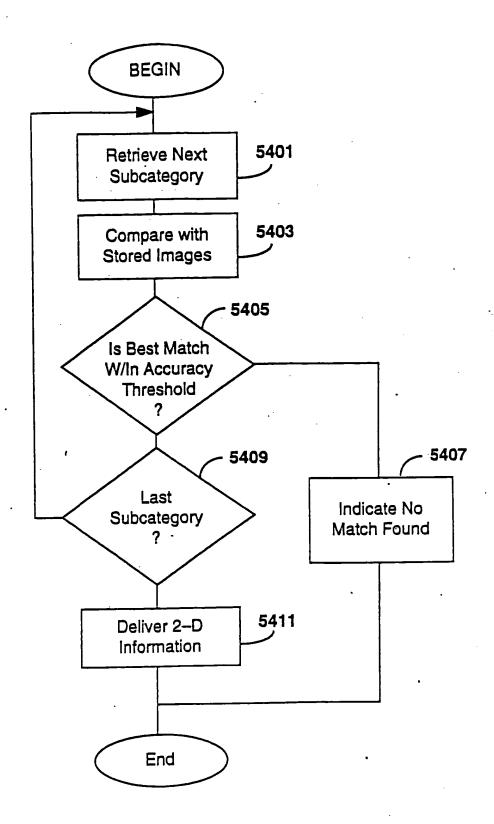


Fig. 54

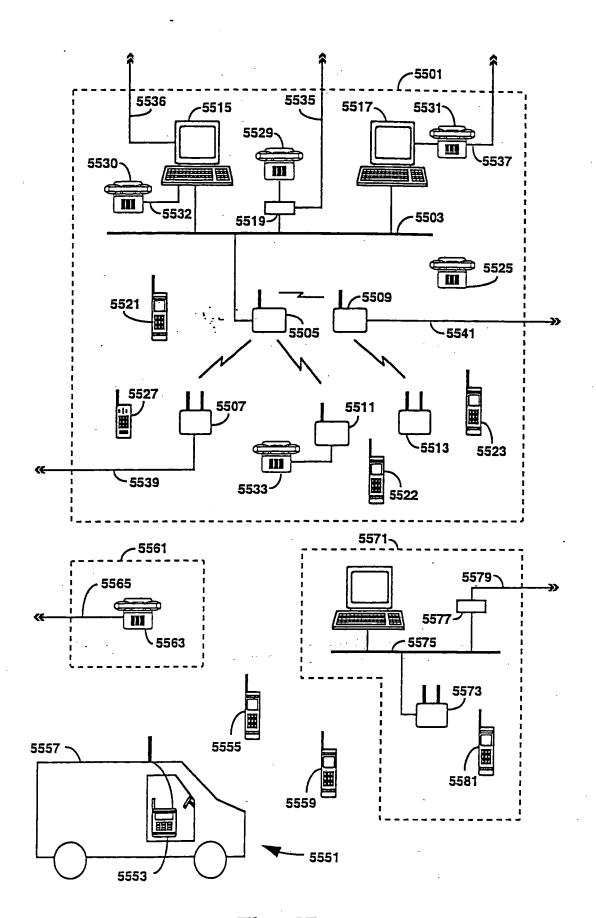


Fig. 55a

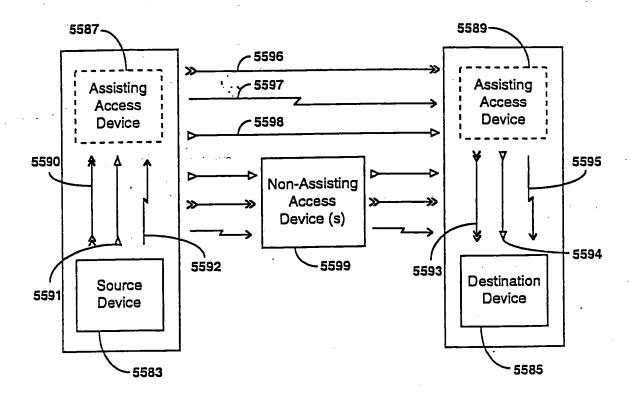


Fig. 55b

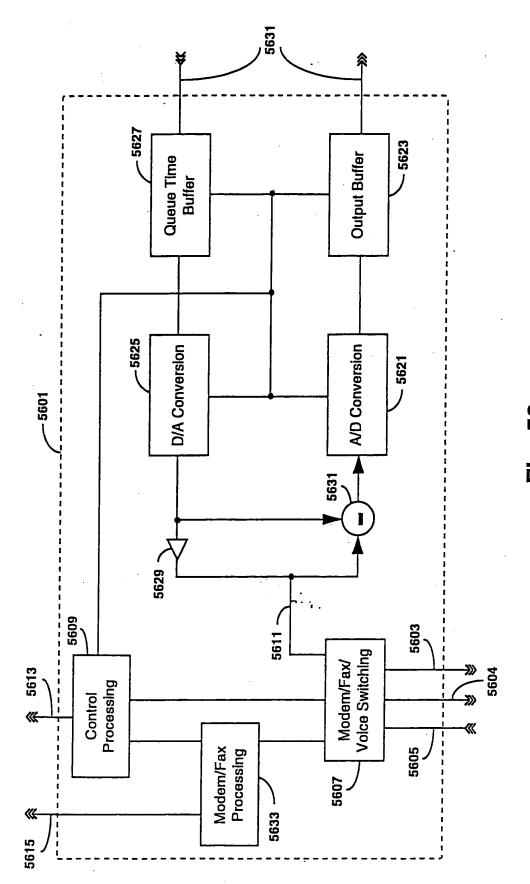


Fig. 56a

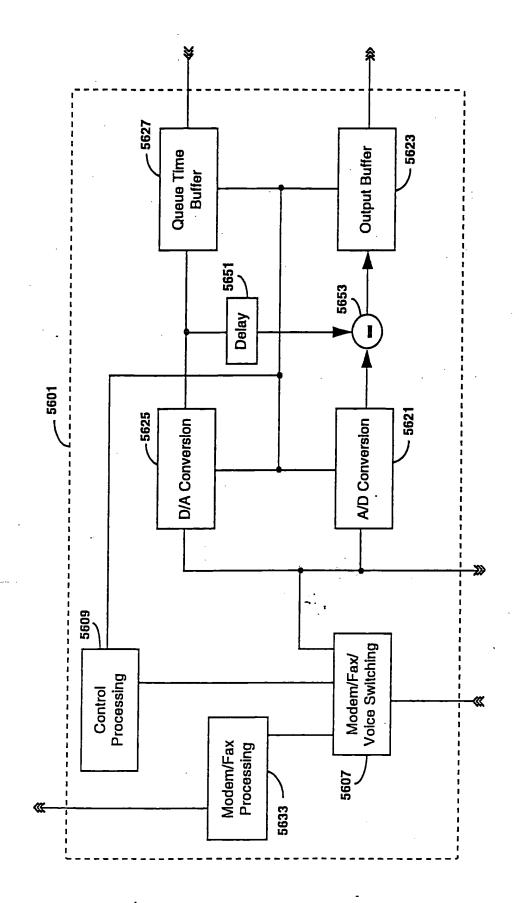


Fig. 56b

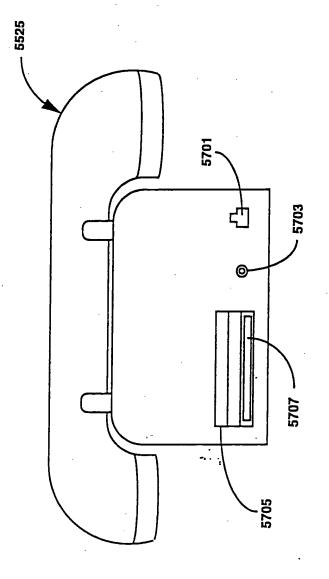


Fig. 57

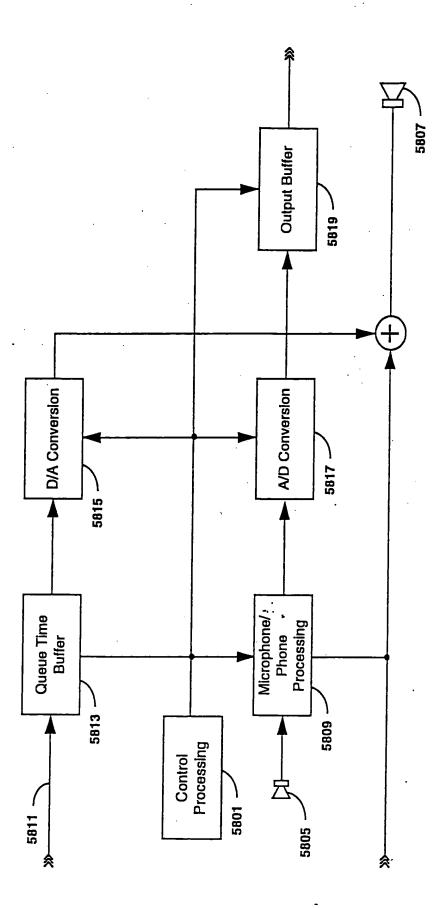


Fig. 58

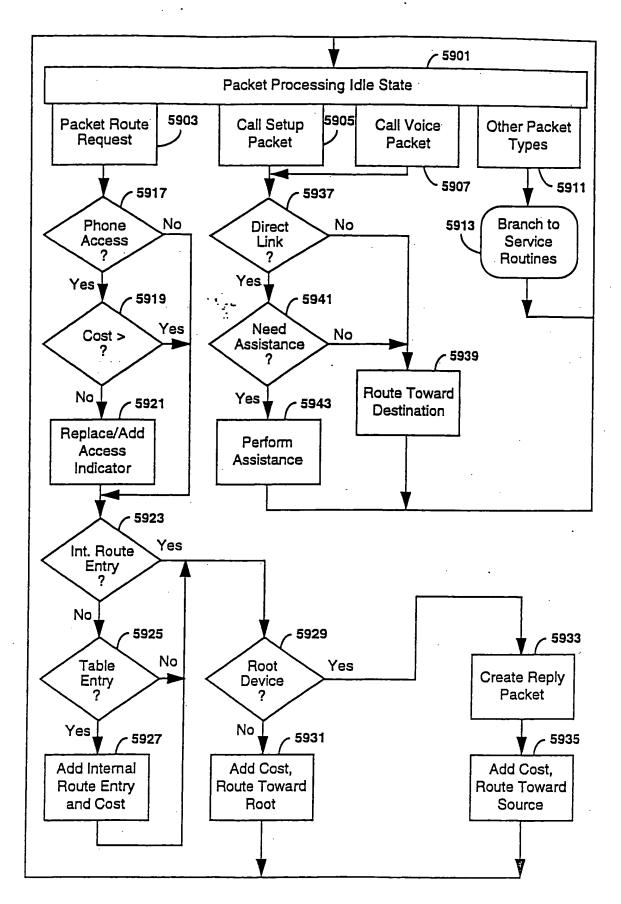


Fig. 59

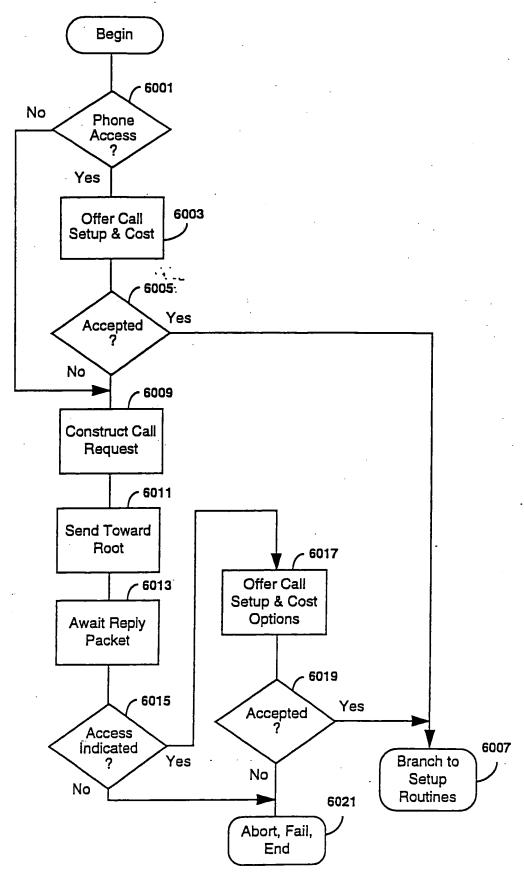


Fig. 60

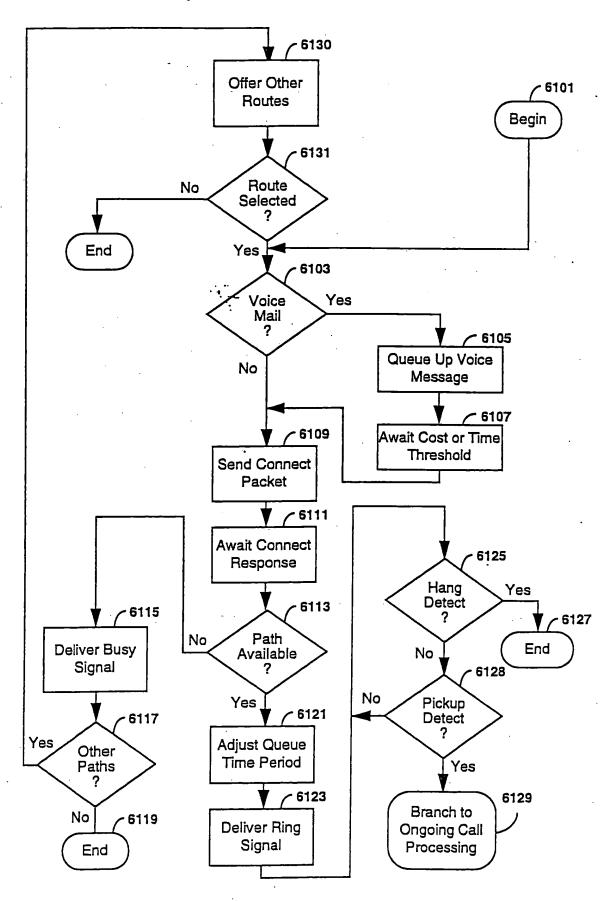


Fig. 61

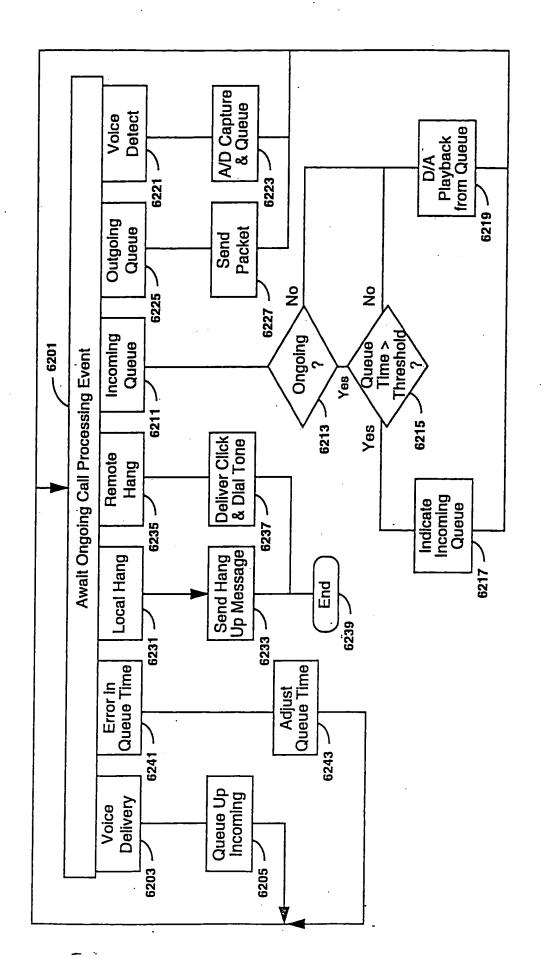


Fig. 62

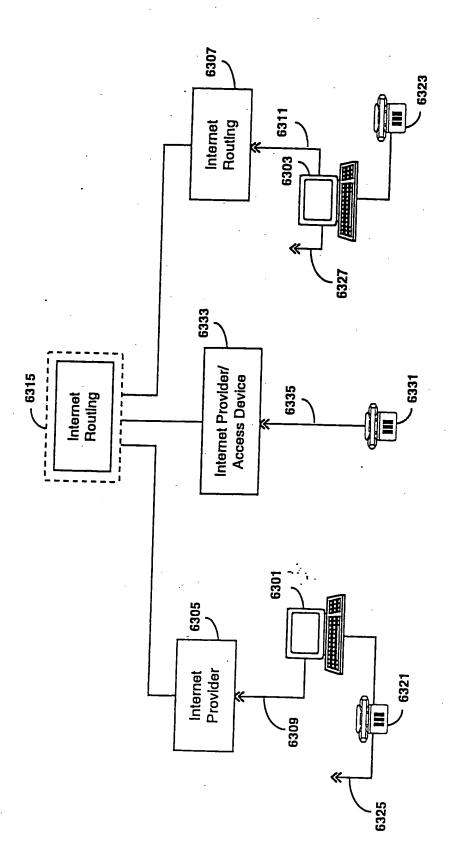


Fig. 63